

REMARKS

This responds to the Office Action mailed on September 2, 2005. Claims 1 and 7-10 are amended, no claims are canceled, and no claims are added. Thus, claims 1-64 remain pending in this application. The amendments to claims 1 and 7 are intended to further clarify the recited cache memory device. These are believed to be supported at least at page 6 lines 6-9 and by FIG. 2 and the corresponding description. The amendments to claims 8-10 are made to use consistent language with respect to the language recited in claim 7.

Information Disclosure Statement

Applicant is submitting an Information Disclosure Statement and a 1449 Form along with this Response. Applicant respectfully requests that an initialed copy of the 1449 Form be returned to Applicant's Representatives to indicate that the references have been considered by the Examiner.

§102 Rejection of the Claims

Claims 1-5, 7, 9 and 10 were rejected under 35 U.S.C. § 102(e) as being anticipated by Diehl et al. (U.S. 6,219,725). Applicant respectfully traverses for the following reasons.

The originally-filed claims 1-10 recite a cache memory device. The rejection admits that memory (130) is not specifically indicated to be a cache memory. Further, originally-filed claim 1, for example, recites a cache memory device comprising, among other things, at least one register. The rejection refers to Diehl and asserts that element 130 represents the memory device (with inherent memory cells) and that elements 310, 320 . . . represent registers. Applicant respectfully submits that the elements 310, 320 . . . are illustrated as part of the DMA system 350 and are not illustrated as part of memory 130. Thus, this §102 rejection of the originally-filed claims is not proper.

Independent claims 1 and 7 have been amended to further clarify the recited cache memory device. With respect to amended independent claim 1, Applicant is unable to find, among other things, in the cited portion of the reference cache memory device, comprising a plurality of memory cells adapted for caching array data traffic separate from other data traffic, and at least one register adapted for storing access information for accessing at least one array

cached in the plurality of memory cells, as recited in the claim. With respect to amended independent claim 7, Applicant is unable to find, among other things, in the cited portion of the reference a cache memory device comprising a plurality of memory cells adapted for caching array data traffic separate from other data traffic, and at least one register adapted for storing access information for accessing at least one array cached in the plurality of memory cells, where the access information includes an array ID for identifying an array of memory cells within the plurality of memory cells, array height information, array width information, and array stride information, as recited in the claim. Claims 2-6 and 8-10 depend either on independent claim 1 or claim 7, and thus are believed to be in condition for allowance therewith.

Applicant respectfully requests the Examiner to withdraw the rejection, and reconsider and allow claims 1-10.

Allowable Subject Matter

Claims 11-64 were allowed.

Reservation of Rights

Applicant maintains its right to swear behind any references relied upon for a rejection under 35 U.S.C. §§102(a), 102(e), 103/102(a), and 103/102(e). Statements distinguishing the claimed subject matter over the cited references are not to be interpreted as admissions that the documents are prior art.

CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 373-6960 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.


Respectfully submitted,

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By their Representatives,

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Date 11-30-05

By 
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 30 day of November, 2005.



Name



Signature